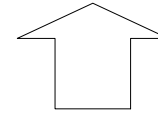


WS2126 Version with jumpers for the SHARC Link Bus on solder side

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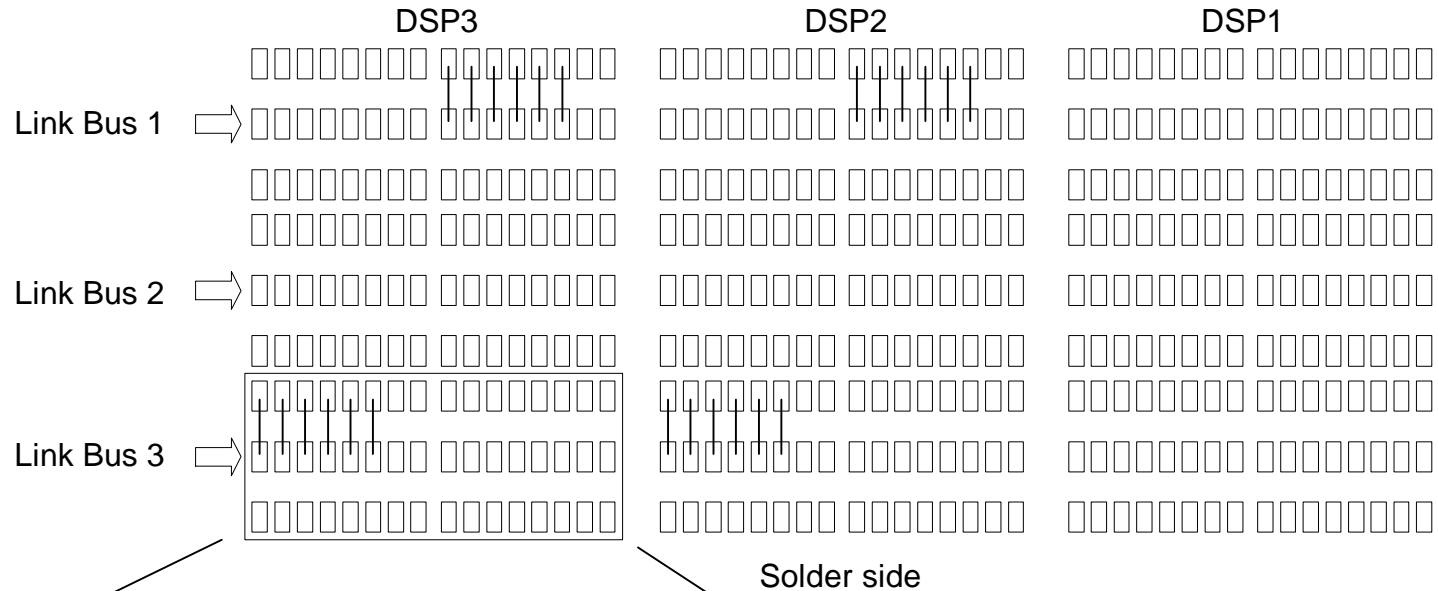
Version PSI Dr. Schilcher



VMEbus Connectors

The illustration on the right side shows the following configuration:

SHARC 1 has no connections to any of the link busses.
 SHARC2 uses Link Bus1 for Link2 and Link Bus3 for Link4.
 On DSP slot3 - there is actually no DSP - the signals from Link Bus1 are routed to DSP Link2 and the signals from Link Bus3 are routed to Link4.



Frontpanel

